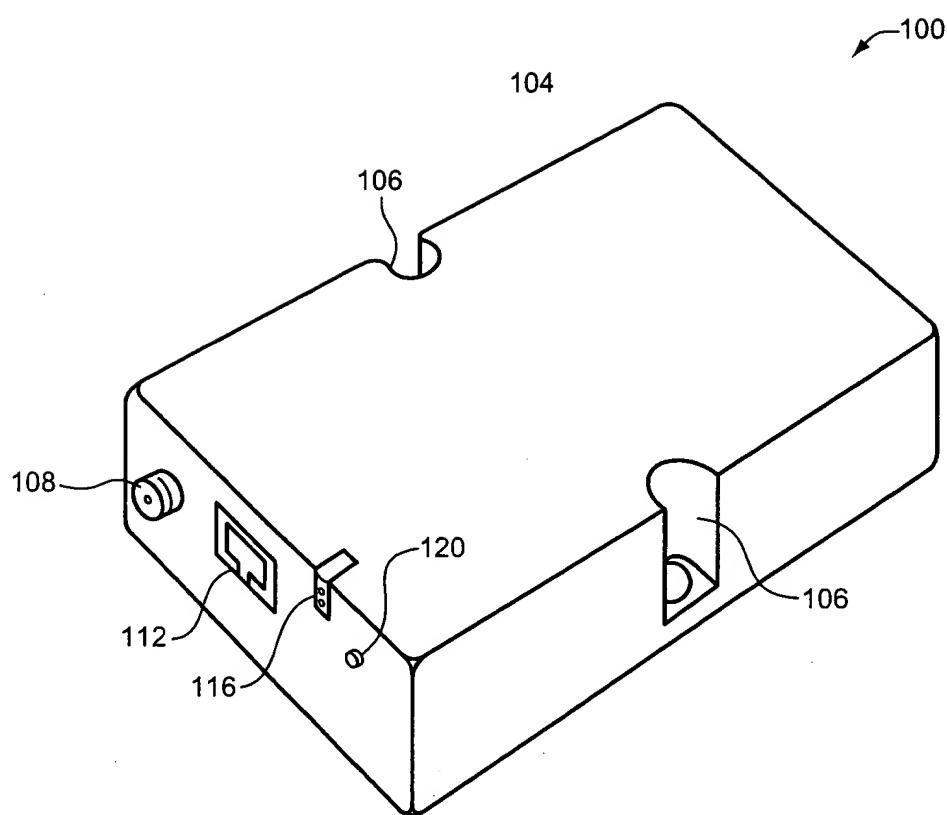




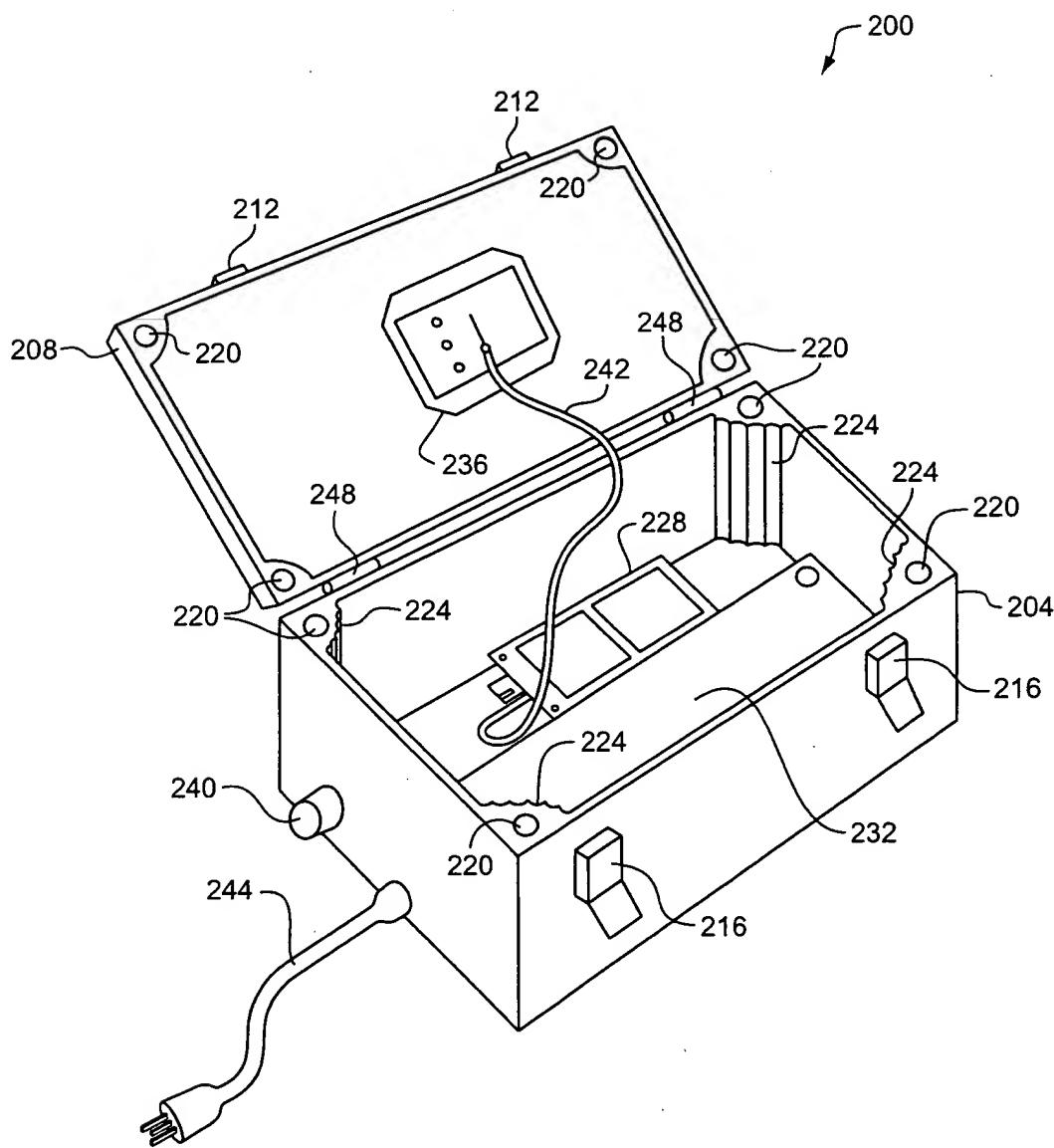
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**FIG. 1**



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**FIG. 2**



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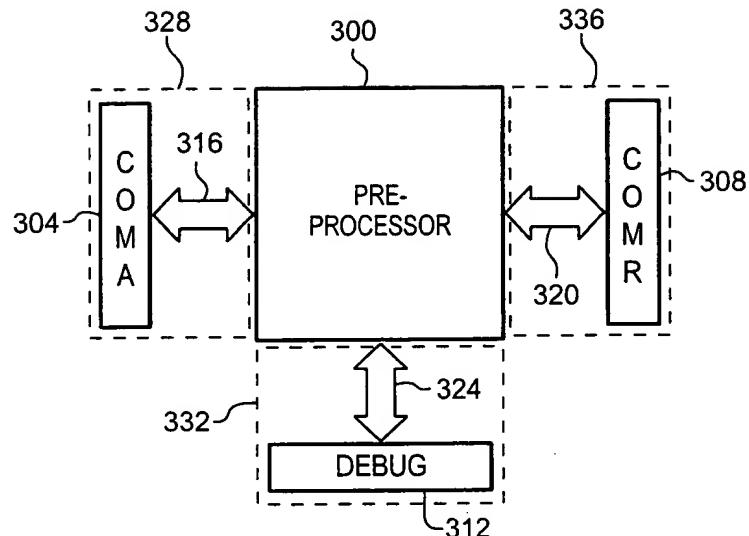


FIG. 3

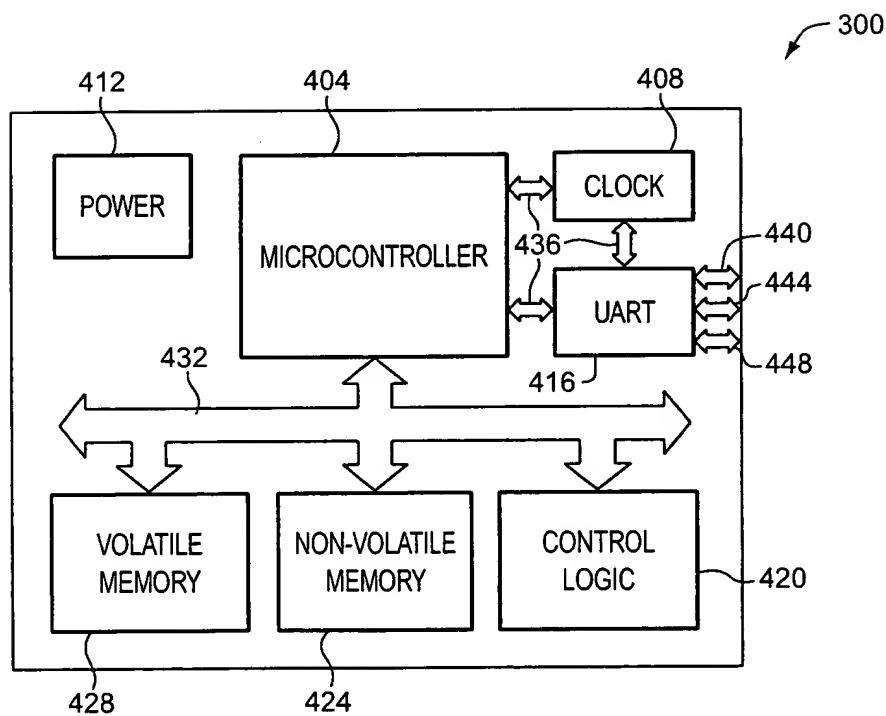
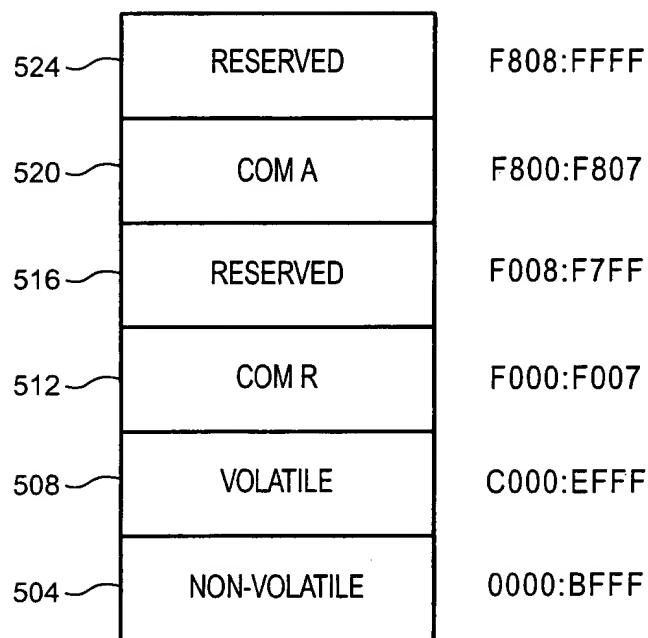


FIG. 4



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500

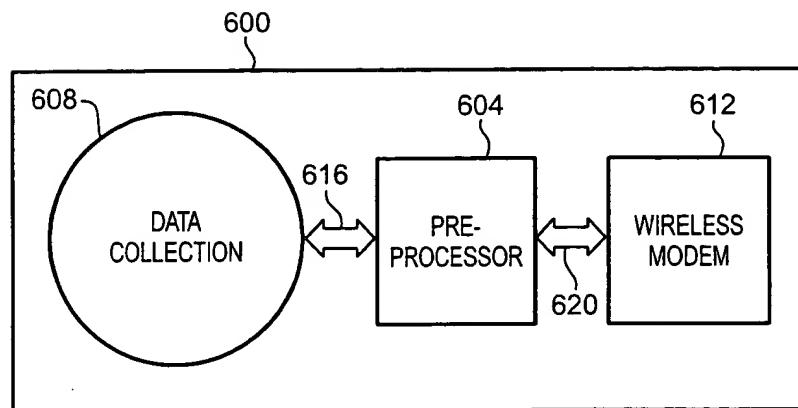


MEMORY MAP

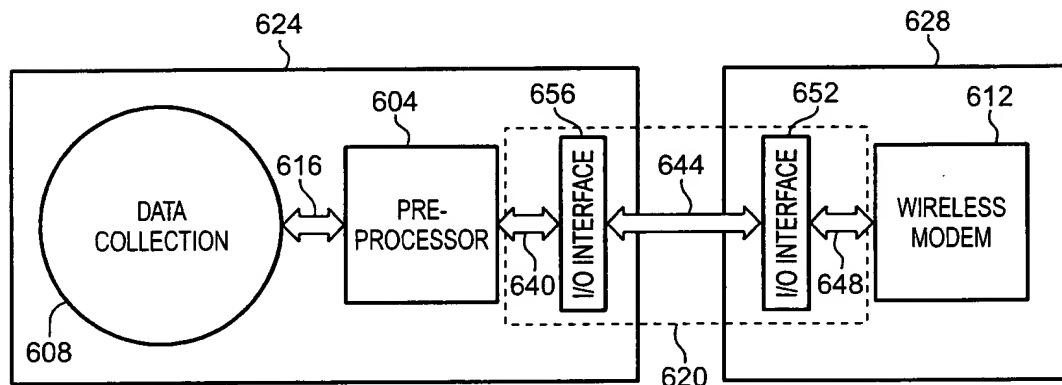
**FIG. 5**



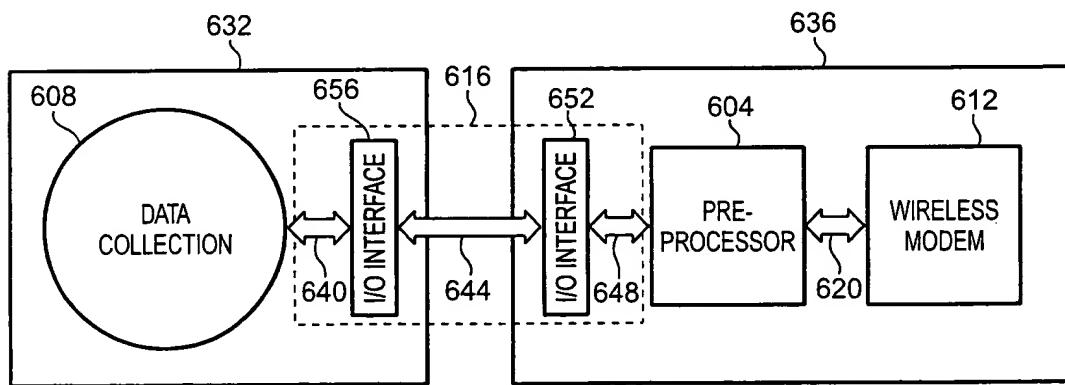
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**FIG. 6A**



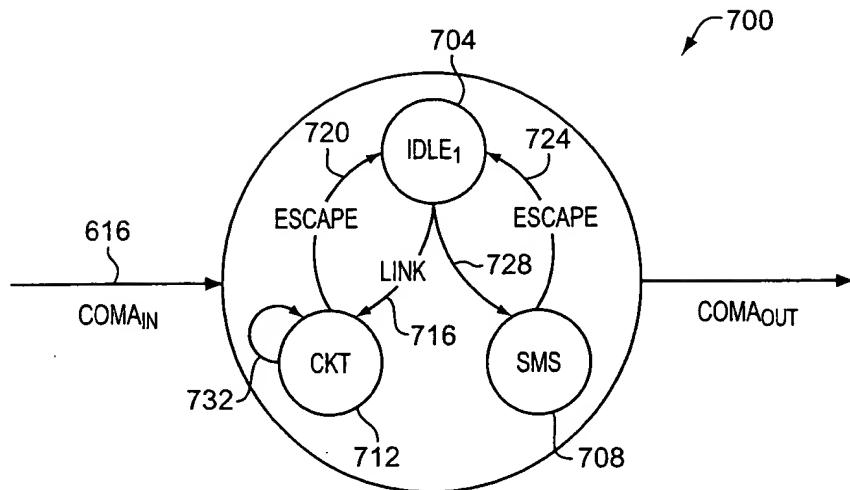
**FIG. 6B**



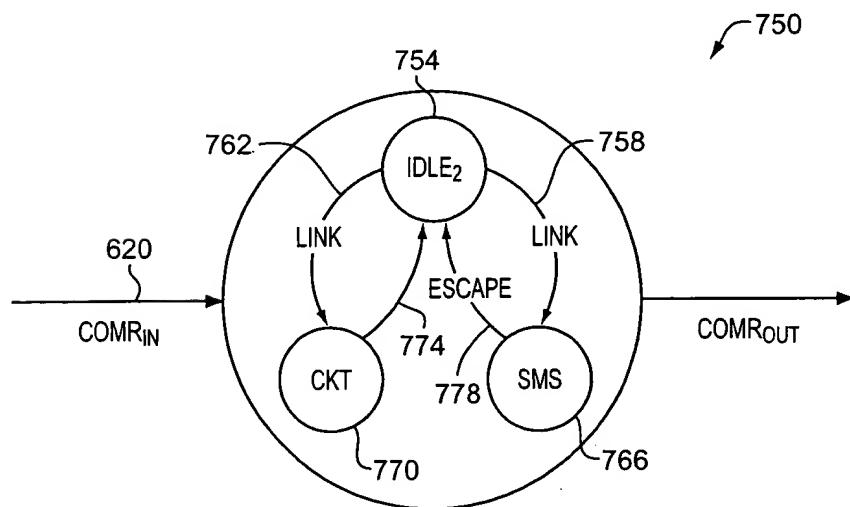
**FIG. 6C**



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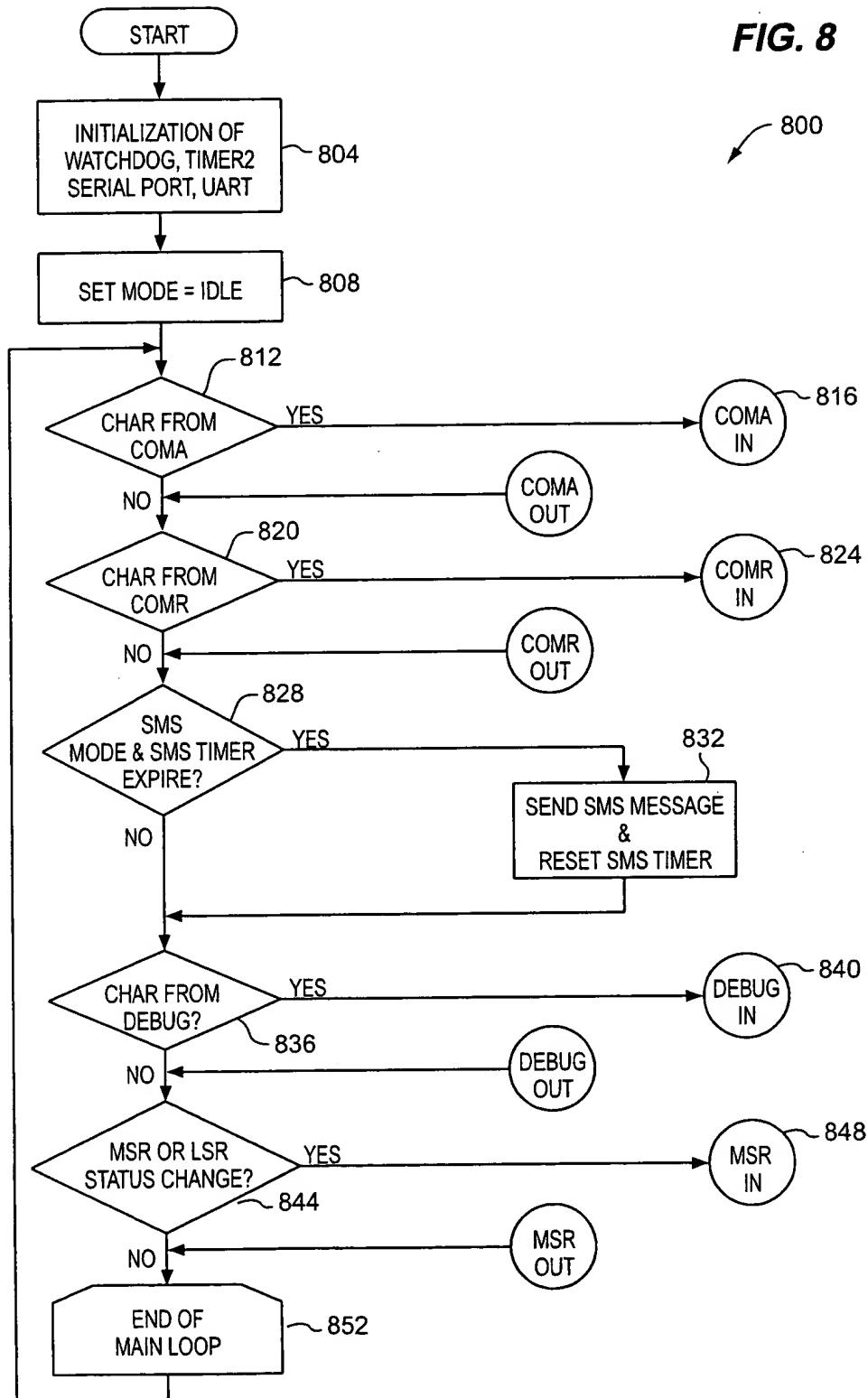
**FIG. 7A**



**FIG. 7B**



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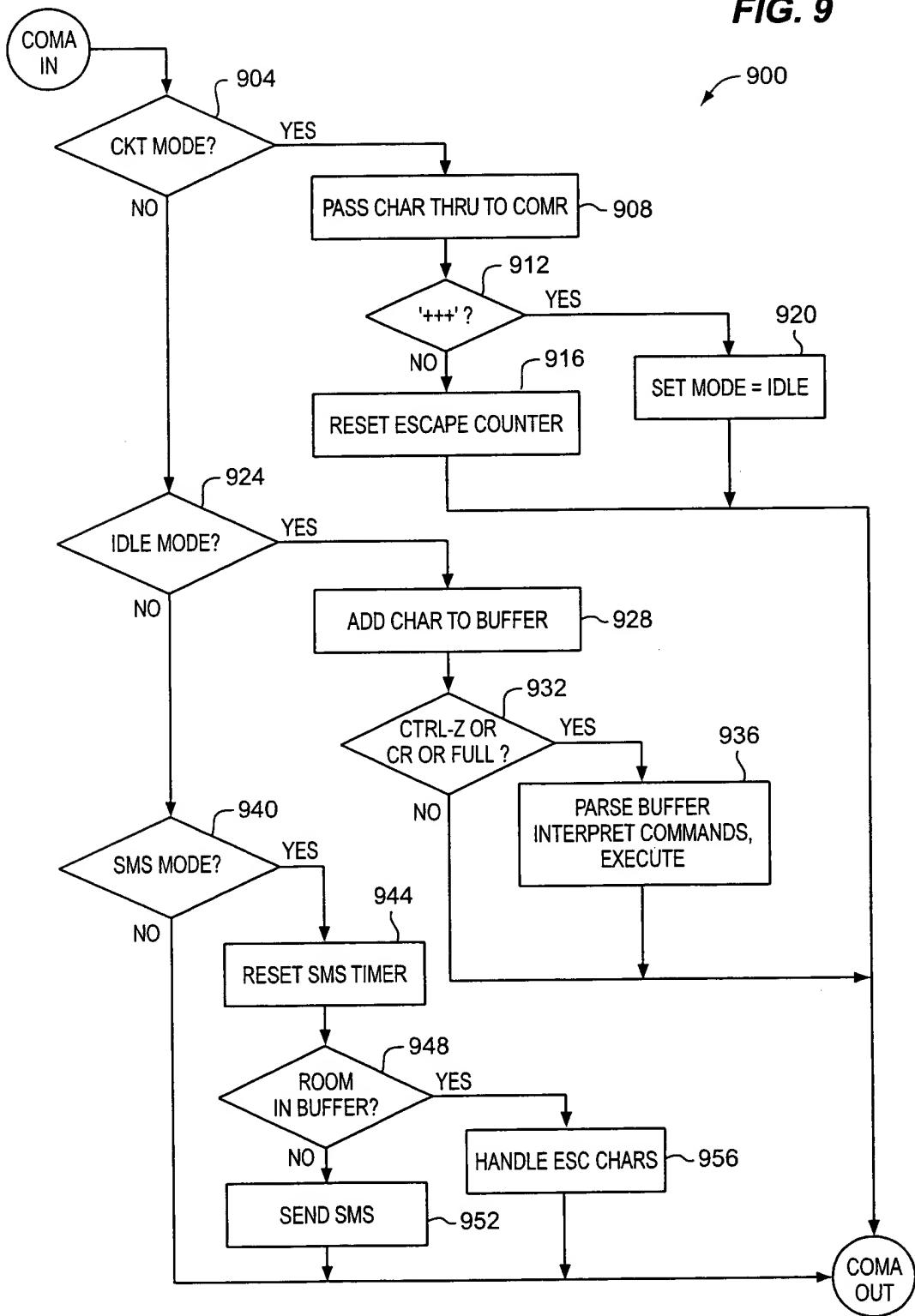


**FIG. 8**



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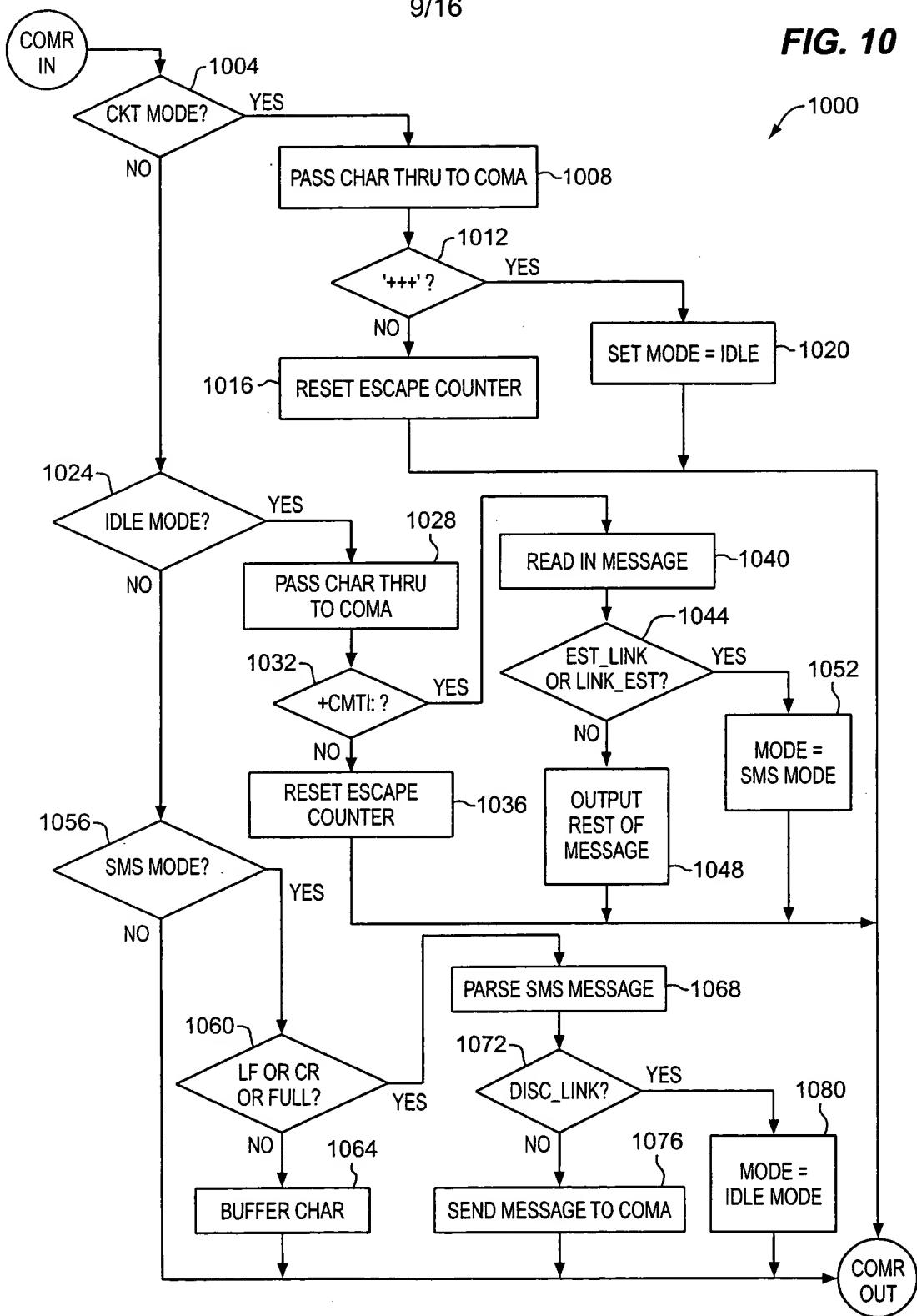
**FIG. 9**





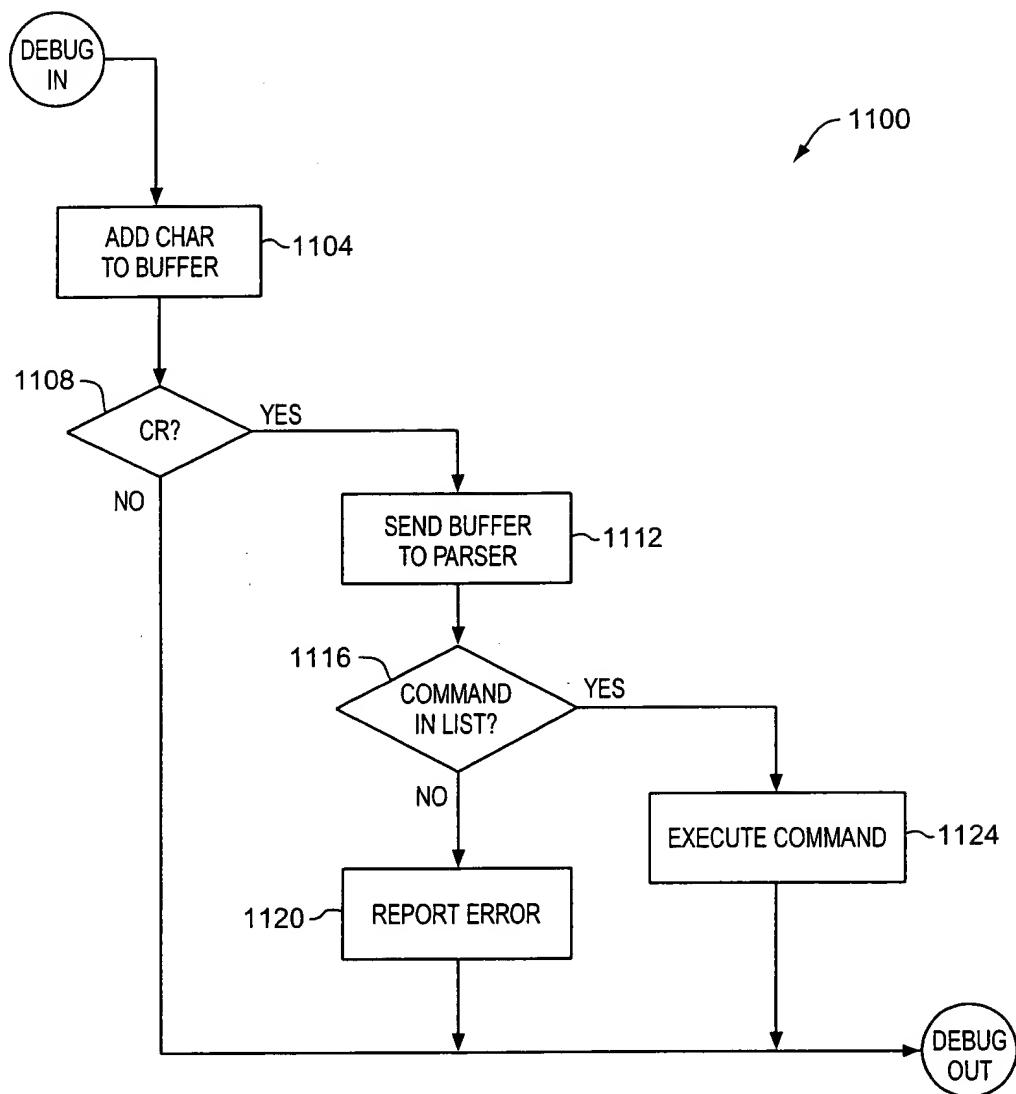
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**FIG. 10**





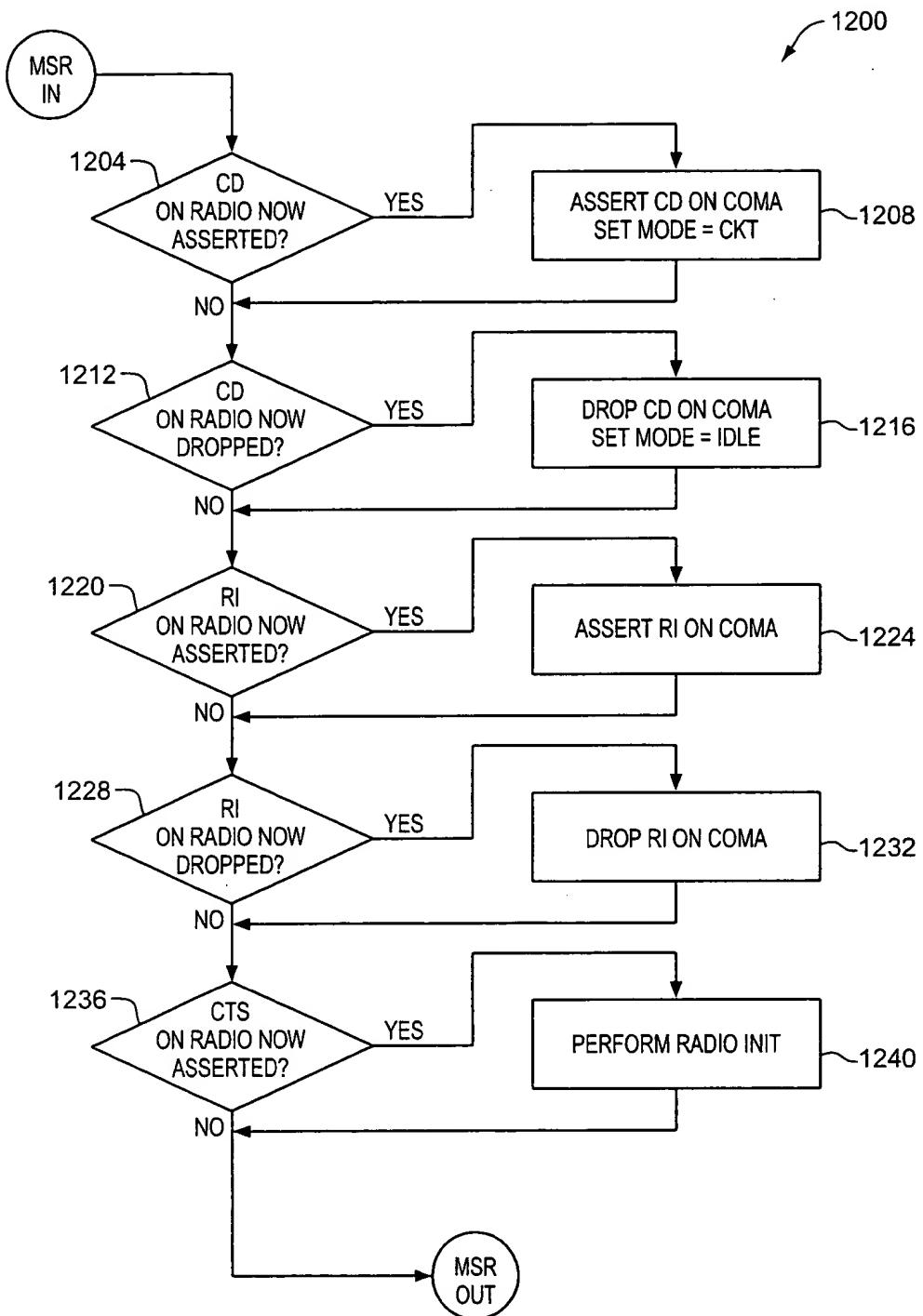
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**FIG. 11**



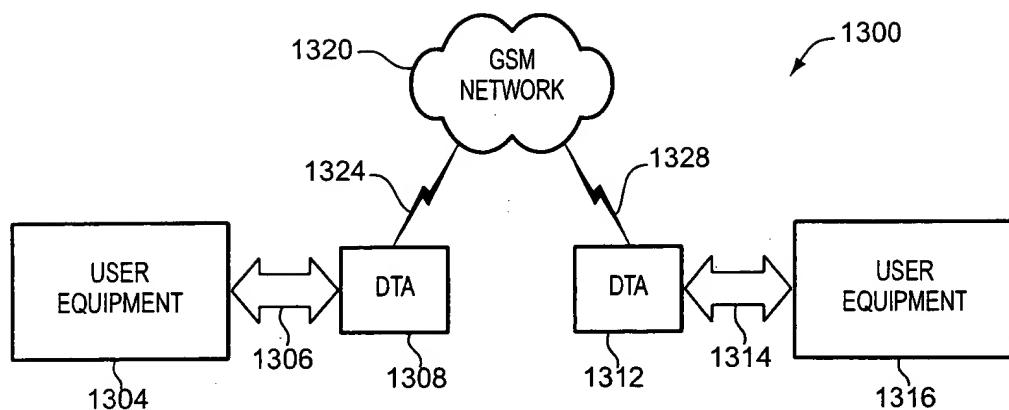
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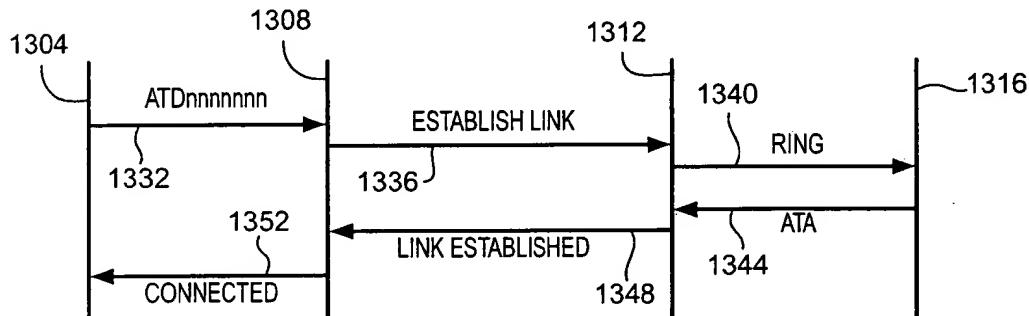
**FIG. 12**



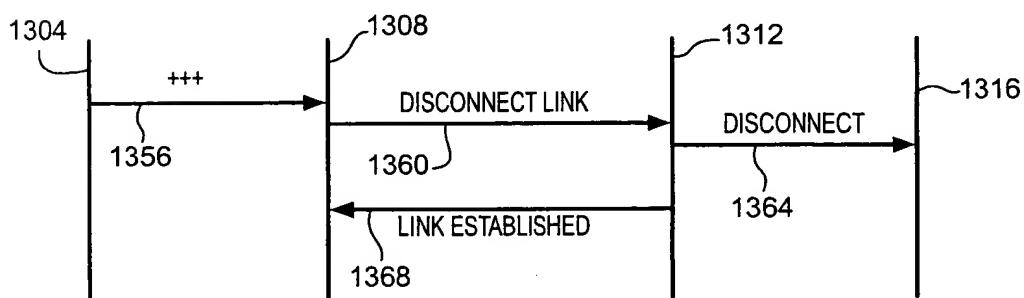
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**FIG. 13A**



**FIG. 13B**



**FIG. 13C**



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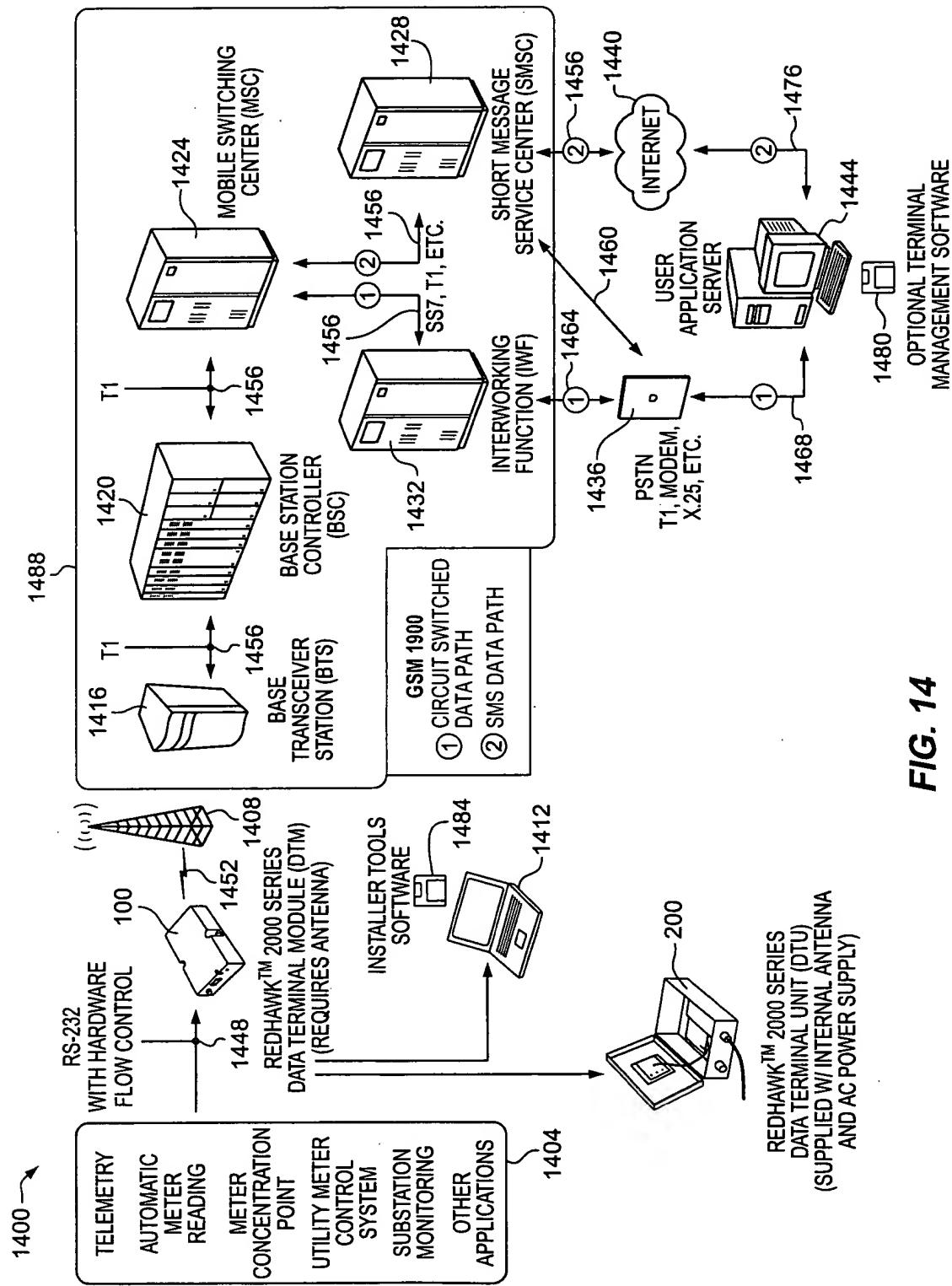
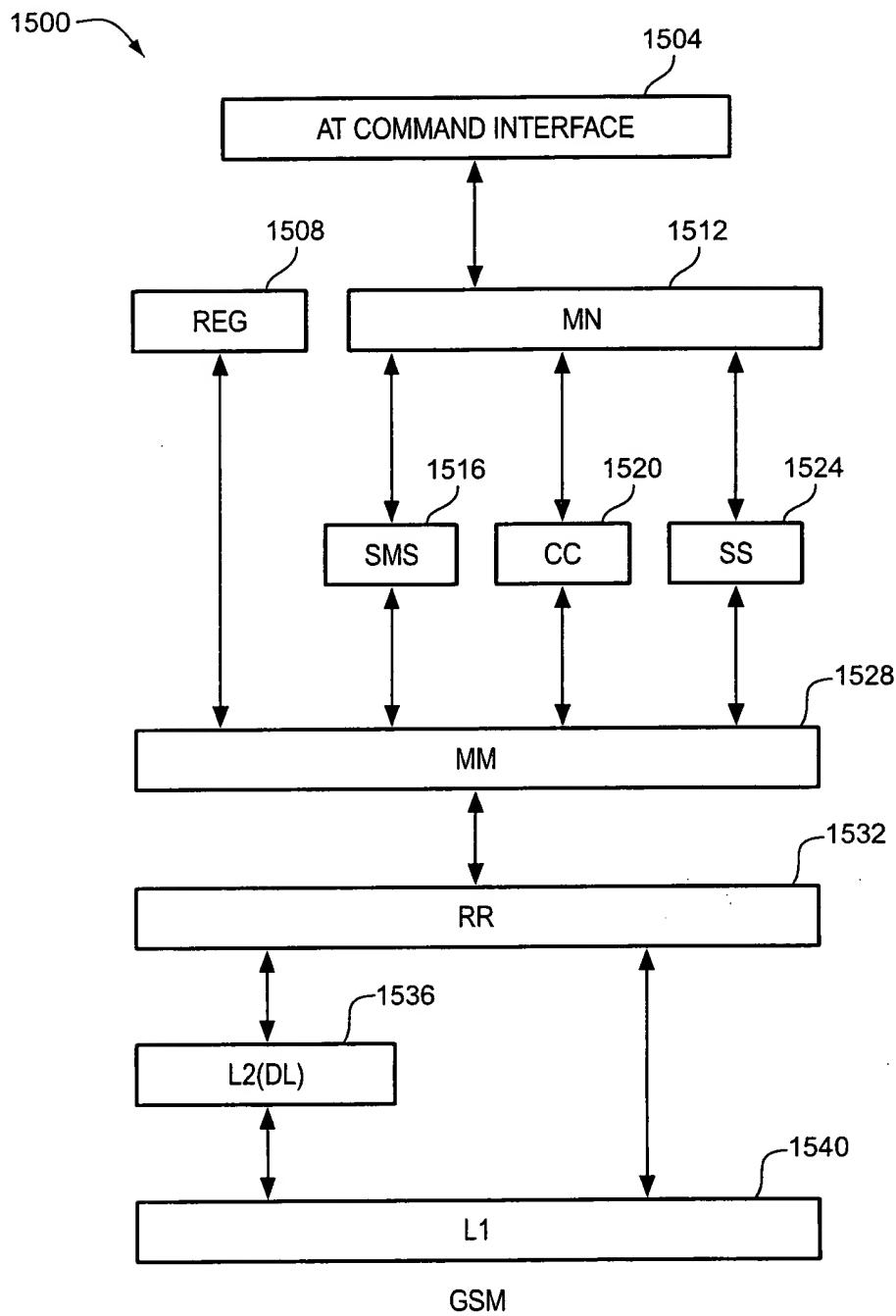


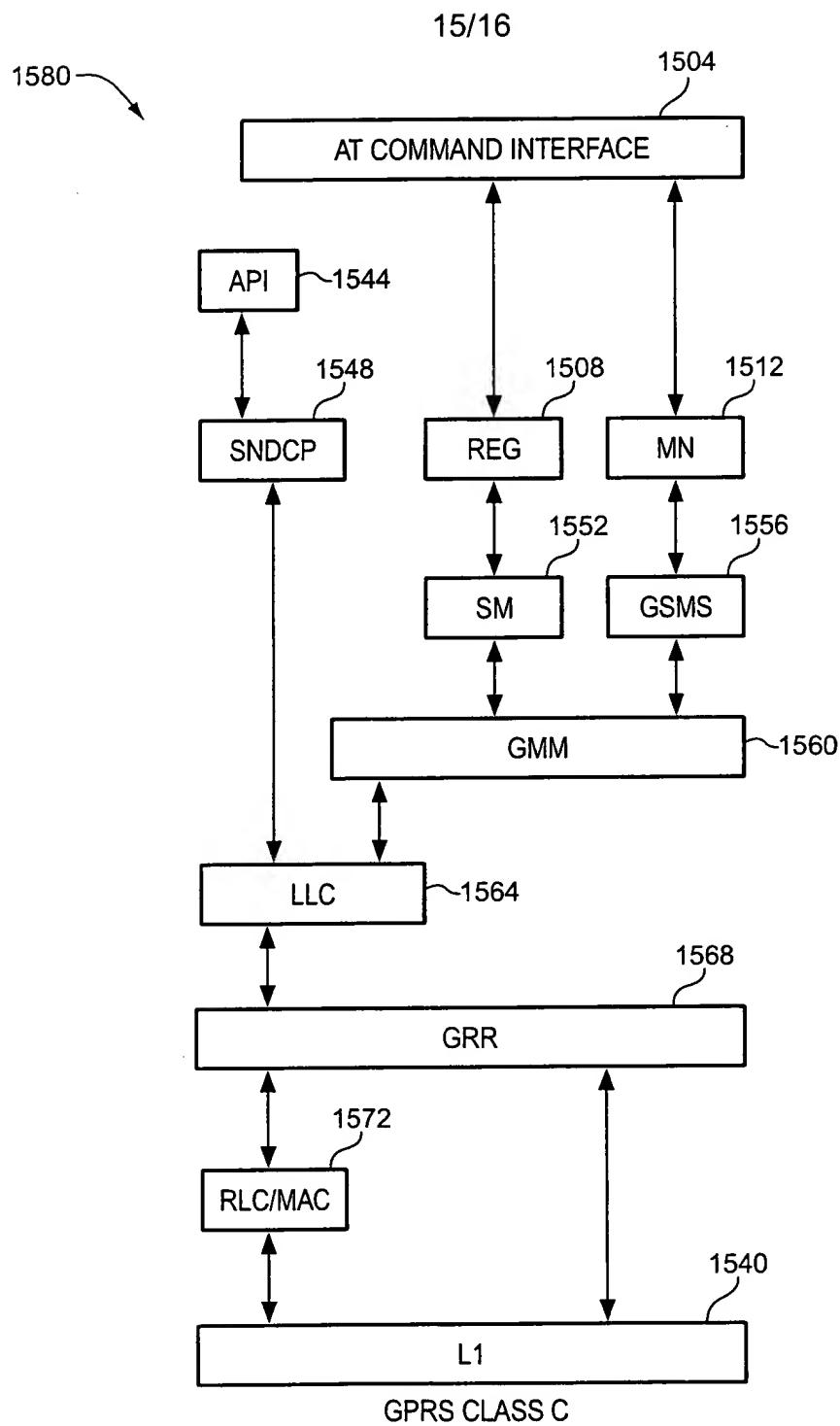
FIG. 14



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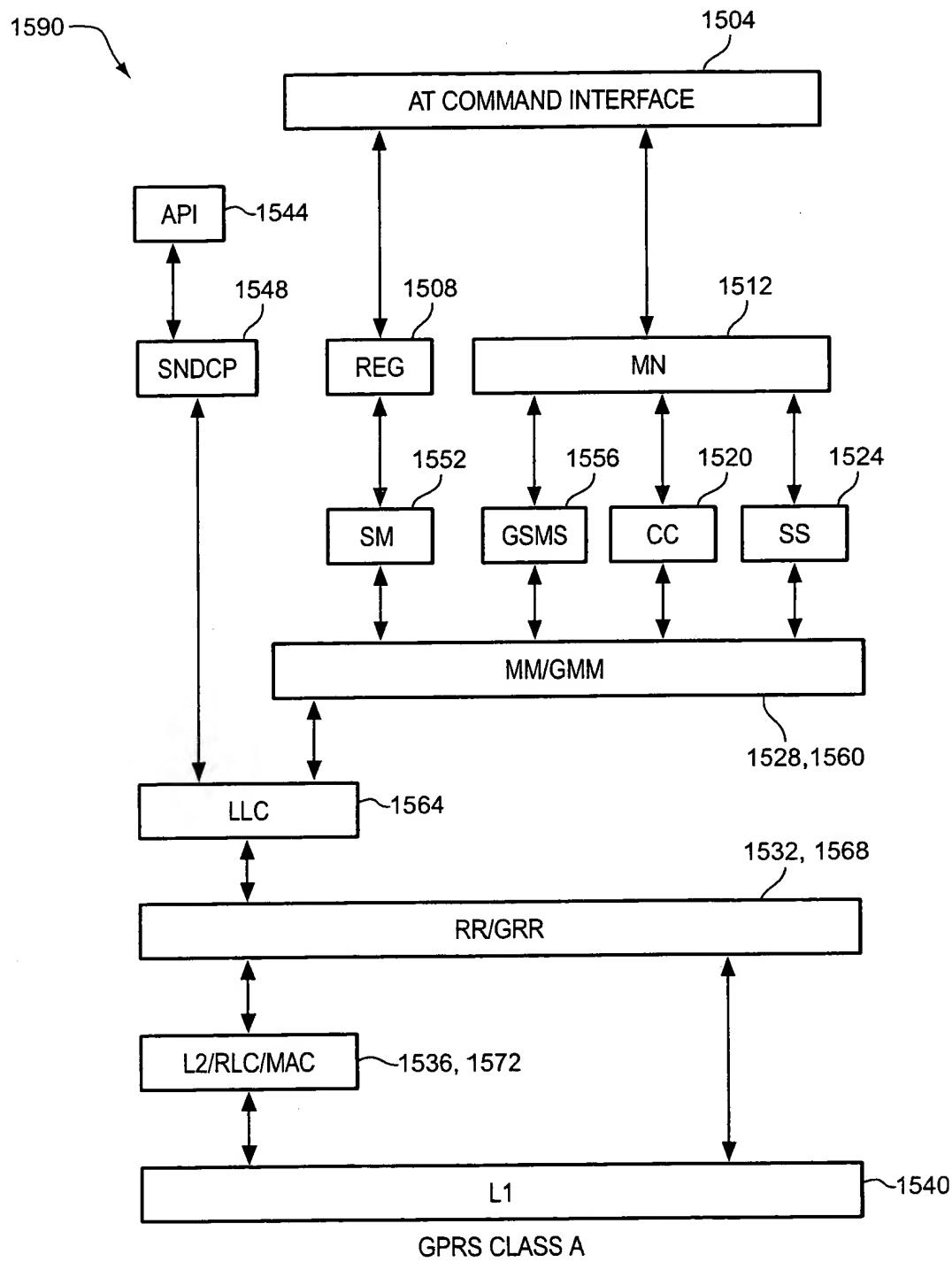
**FIG. 15A**



**FIG. 15B**



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**FIG. 15c**